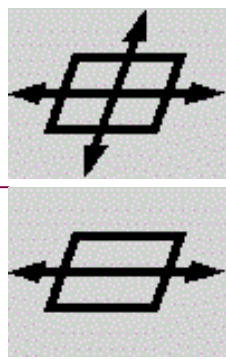
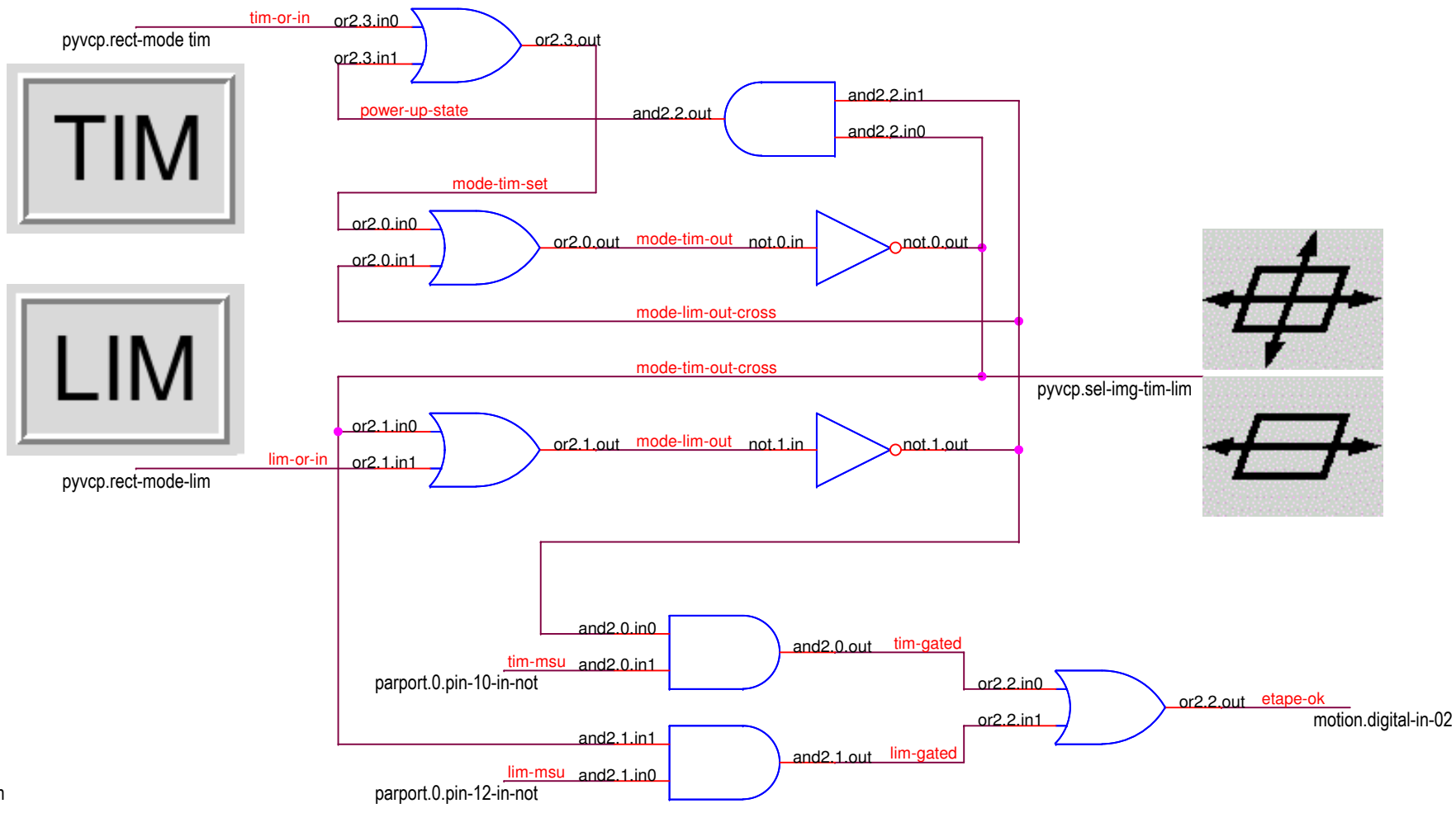


```

loadrt and2 count=3
loadrt or2 count=4
loadrt not count=2
addf and2.0 servo-thread
addf and2.1 servo-thread
addf and2.2 servo-thread
addf or2.0 servo-thread
addf or2.1 servo-thread
addf or2.2 servo-thread
addf or2.3 servo-thread
addf not.0 servo-thread
addf not.1 servo-thread
net tim-or-in pyvcp.rect-mode-tim
net tim-or-in or2.3.in0
net power-up-state and2.2.out
net power-up-state or2.3.in1
net mode-tim-set or2.3.out
net mode-tim-out or2.0.out
net mode-tim-out-cross not.0.in
net mode-tim-out-cross not.0.out
net mode-tim-out-cross not.1.in
net mode-tim-out-cross not.1.out
net mode-tim-out-cross pyvcp.sel-img-tim-lim
net lim-or-in pyvcp.rect-mode-lim
net lim-or-in or2.1.in0
net lim-or-in or2.1.in1
net mode-lim-out not.1.in
net mode-lim-out not.1.out
net tim-msu parport.0.pin-10-in-not
net tim-msu and2.0.in1
net mode-lim-out-cross not.1.out
net mode-lim-out-cross or2.0.in1
net mode-lim-out-cross and2.0.in1
net mode-lim-out-cross and2.2.in1
net mode-tim-out-cross or2.1.in0
net mode-tim-out-cross not.0.out
net mode-tim-out-cross and2.0.in0
net mode-tim-out-cross and2.2.in0
net mode-tim-out-cross pyvcp.sel-img-tim-lim
net lim-msu parport.0.pin-12-in-not
net lim-msu and2.1.in0
net tim-gated and2.0.out
net tim-gated or2.2.in0
net lim-gated and2.1.out
net lim-gated or2.2.in1
net etape-ok or2.2.out
net etape-ok motion.digital-in-02

```



HAL replica Interface - example