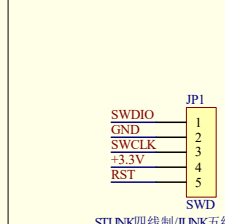
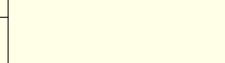
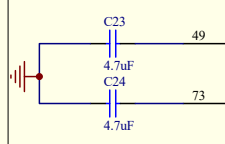
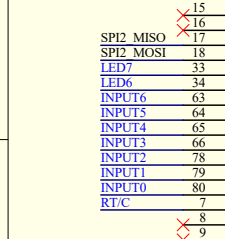
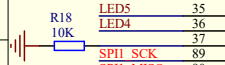
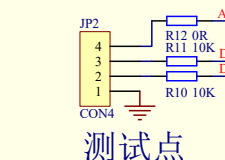
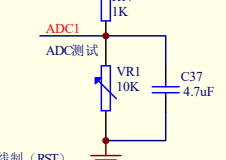


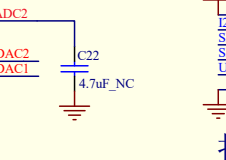
SYNCO	23	PA0/WKUP/USART2_CTS/ADC123_IN0/TIM2_CH1_ETR/TIM5_CH1/TIM8_ETR	PD0/OSC_IN/FSMC_D2/CAN_RX	81
SYNCl	24	PA1/USART2_RTS/ADC123_IN1/TIM5_CH2/TIM2_CH2	PD1/OSC_OUT/FSMC_D3/CAN_TX	82
USART2_TX	25	PA2/USART2_TX/TIM5_CH3/ADC123_IN2/TIM2_CH3	PD2/TIM3_ETR/UART5_RX/SDIO_CMD	83
USART2_RX	26	PA3/USART2_RX/TIM5_CH4/ADC123_IN3/TIM2_CH4	PD3/FSMC_CLK/USART2_CTS	84
DAC1	29	PA4/SPI1_NSS/DAC_OUT1/ADC12_IN4	PD4/SPI1_NSS/DAC_OUT1/ADC12_IN4	85
DAC2	30	PA5/SPI1_SCK/DAC_OUT2/ADC12_IN5	PD5/FSMC_NWE/USART2_TX	86
ADC2	31	PA6/SPI1_MISO/TIM8_BKIN/ADC12_IN6/TIM3_CH1/TIM1_BKIN	PD6/FSMC_NWAIT/USART2_RX	87
ADC1	32	PA7/SPI1_MOSI/TIM8_CH1N/ADC12_IN7/TIM3_CH2/TIM1_CH1N	PD7/FSMC_NE1/FSMC_NCE2/USART2_CK	88
SPI_IRQ	67	PA8/USART1_CK/TIM1_CH1/MCO	PD8/FSMC_D13/USART3_TX	89
USART1_TX	68	PA9/USART1_TX/TIM1_CH2	PD9/FSMC_D14/USART3_RX	90
USART1_RX	69	PA10/USART1_RX/TIM1_CH3	PD10/FSMC_D15/USART3_CK	91
CANRX	70	PA11/USART1_CTS/USBDM/CAN_RX/TIM1_CH4	PD11/FSMC_A16/USART3_CTS	92
CANTX	71	PA12/USART1_RTS/USBDP/CAN_TX/TIM1_ETR	PD12/FSMC_A17/TIM4_CH1/USART3_RTS	93
SWDIO	72	PA13/JTMS/SWDIO/	PD13/FSMC_A18/TIM4_CH2	94
SWCLK	76	PA14/JTCK/SWCLK	PD14/FSMC_D0/TIM4_CH3	95
SPI1_NSS	77	PA15/JTD1/SPI3_NSS/I2S3_ws	PD15/FSMC_D1/TIM4_CH4	96
LED5	35	PB0/ADC12_IN8/TIM3_CH3/TIM8_CH2N/TIM1_CH2N	PE0/TIM4_ETR/FSMC_NBL0/FSMC_NBL0	97
LED4	36	PB1/ADC12_IN9/TIM3_CH4/TIM8_CH3N/TIM1_CH3N	PE1/FSMC_NBL1	98
SPI1_SCK	89	PB2/BOOT1	PE2/TRACECK/FSMC_A23	1
SPI1_MISO	90	PB3/JTDO/SPI3_SCK/I2S3_CK/TRACESWO/TIM2_CH2/SPI1_SCK	PE3/TRACED0/FSMC_A19	2
SPI1_MOSI	91	PB4/NJTRST/SPI3_MISO/TIM3_CH1/SPI1_MISO/TIM3_CH1/SPI1_MISO	PE4/TRACED1/FSMC_A20	3
I2C1_SCL	92	PB5/I2C1_SMB/SPB3_MOSI/I2S3_SD/TIM3_CH2/SPI1_MOSI	PE5/TRACED2/FSMC_A21	4
I2C1_SDA	93	PB6/I2C1_SCL/TIM4_CH1/USART1_TX	PE6/TRACED3/FSMC_A22	5
SPI2_NSS	95	PB7/I2C1_SDA/FSMC_NADV/TIM4_CH2/USART1_RX	PE7/FSMC_D4/TIM1_ETR	6
SPI2_SCK	47	PB8/TIM4_CH3/SDIO_D4/I2C1_SCL/CAN_RX	PE8/FSMC_D5/TIM1_CH1N	7
LED3	48	PB9/TIM4_CH4/SDIO_D5/I2C1_SDA/CAN_TX	PE9/FSMC_D6/TIM1_CH1	8
LED2	51	PB10/I2C2_SCL/USART3_TX/TIM2_CH3	PE10/FSMC_D7/TIM1_CH2N	9
LED1	52	PB11/I2C2_SDA/USART3_RX/TIM2_CH4	PE11/FSMC_D8/TIM1_CH2	10
LED0	53	PB12/SPI2_NSS/I2S2_WS/I2C2_SMB/USART3_CK/TIM1_BKIN	PE12/FSMC_D9/TIM1_CH3N	11
INPUT7	54	PB13/SPI2_SCK/I2S2_CK/USART3_CTS/TIM1_CH1N	PE13/FSMC_D10/TIM1_CH3	12
		PB14/SPI2_MISO/TIM1_CH2N/USART3_RTS	PE14/FSMC_D11/TIM1_CH4	13
		PB15/SPI2_MOSI/I2S2_SD/TIM1_CH3N	PE15/FSMC_D12/TIM1_BKIN	14



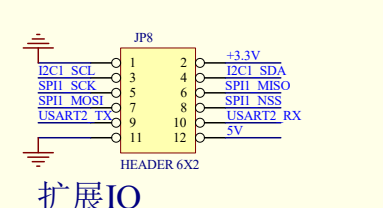
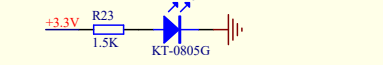
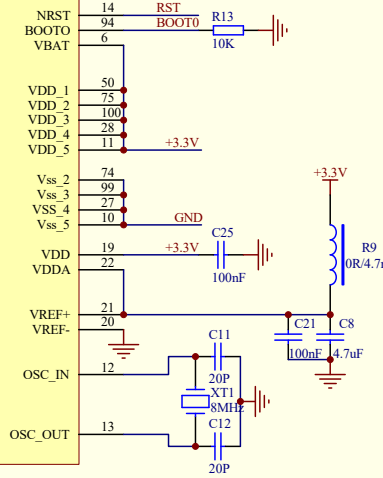
STLINK四线制/JLINK五线制 (RST)



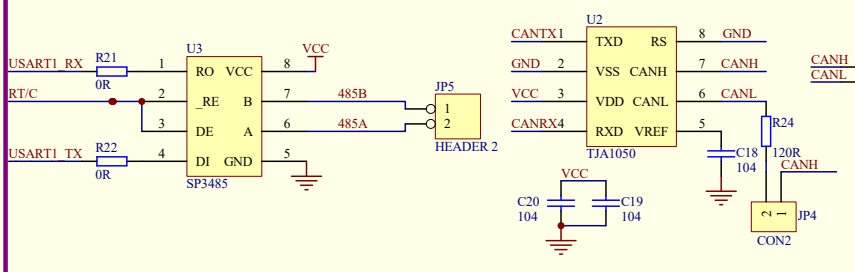
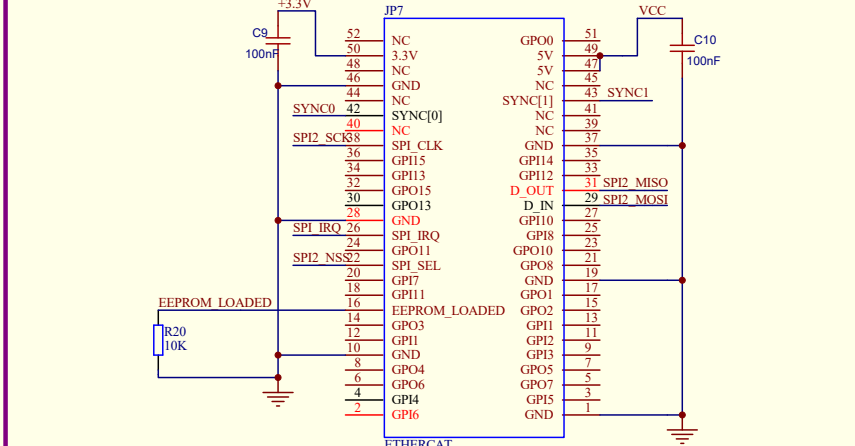
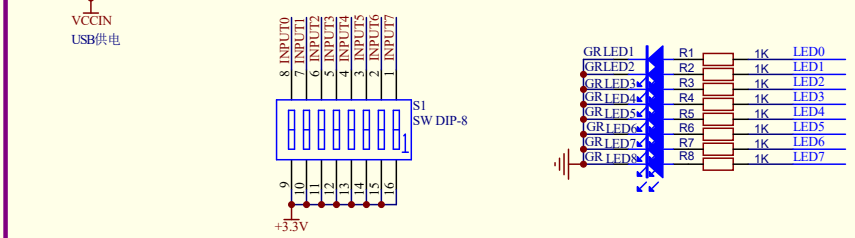
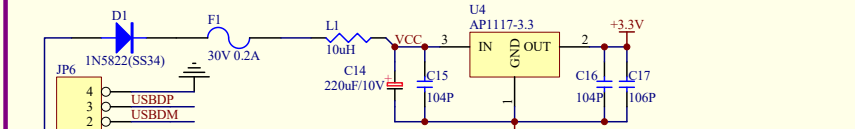
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